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	APPLICANT Takashi YOKOKAWA et al.	
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LIST OF REFERENCES CITED BY APPLICANT  
(Use several sheets if necessary)

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>SwR</i>	AA	6, 539, 367	03/25/03	Blanksby et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
<i>SwR</i>	AL	JP 2003-269302	09/25/03	Japan			
<i>SwR</i>	AM	JP 2002-33670	01/31/02	Japan			
<i>SwR</i>	AN	EP 1 158 682	11/28/01	Europe			
	AO						
	AP						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)			
	AQ		S. Kim et al, 'Parallel VSLI Architectures for a Class of LD PC Codes', IEEE International Symposium on Circuits and Systems, 2002, ISCAS 2002, Volume 2, November 2002, p. 26-29
	AR		
	AS		
EXAMINER <i>SwR</i>		DATE CONSIDERED <i>4/6/06</i>	

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